



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Stephan Grunow, et al.

Docket No: TI-36564

Serial No: 10/689,348

Conf. No: 4206

Examiner: Dana Farahani

Art Unit: 2829

Filed: 10/20/2003

For: LOW K DIELECTRIC INTEGRATED CIRCUIT INTERCONNECT STRUCTURE

LETTER TO OFFICIAL DRAFTSPERSON

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Official Draftsperson

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

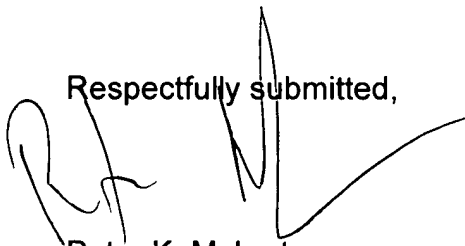
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 6-2-05.


Ann Trent

Dear Sir:

Transmitted herewith for filing are two sheets of formal drawings for the above identified application. Charge any necessary fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



Peter K. McLarty
Attorney for Applicants
Reg. No. 44,923

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-4258



ATTORNEY: PKM
DOCKET NO: TL-36564
DATE: 5-27-5
DUE DATE: 5-31-5

TEXAS INSTRUMENTS PATENT DRAFTING

Michael Jenkins
972-917-5639
